

FIG. 1

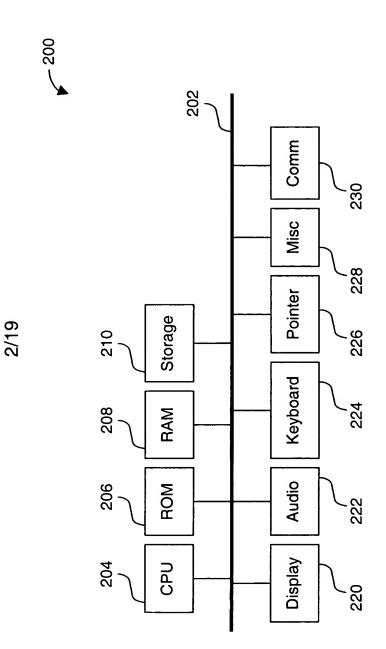


FIG. 2

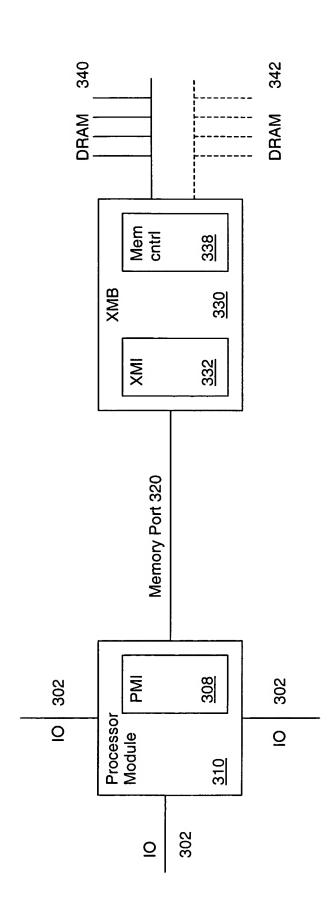


FIG. 3

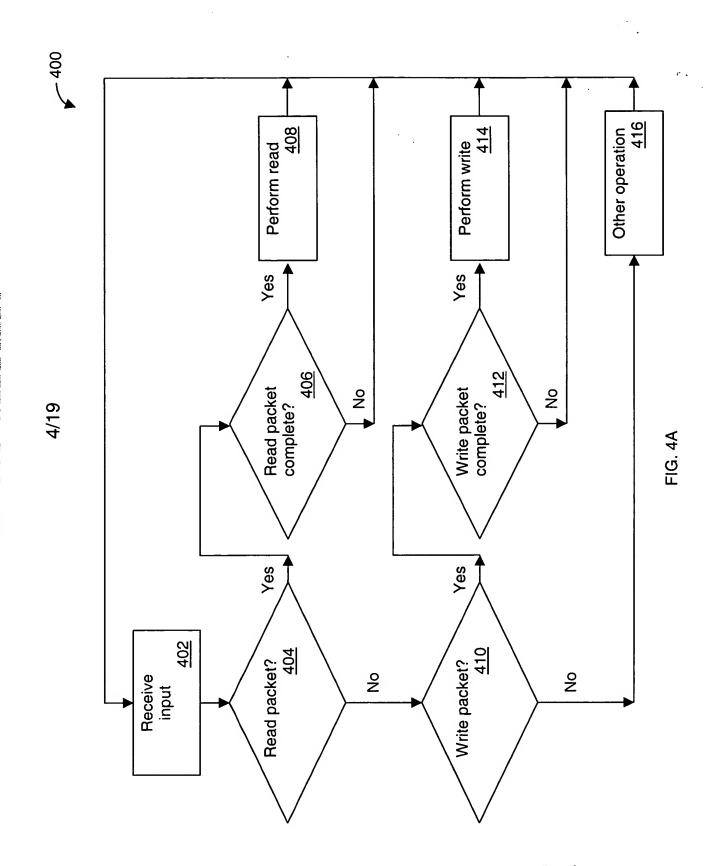


FIG. 4B

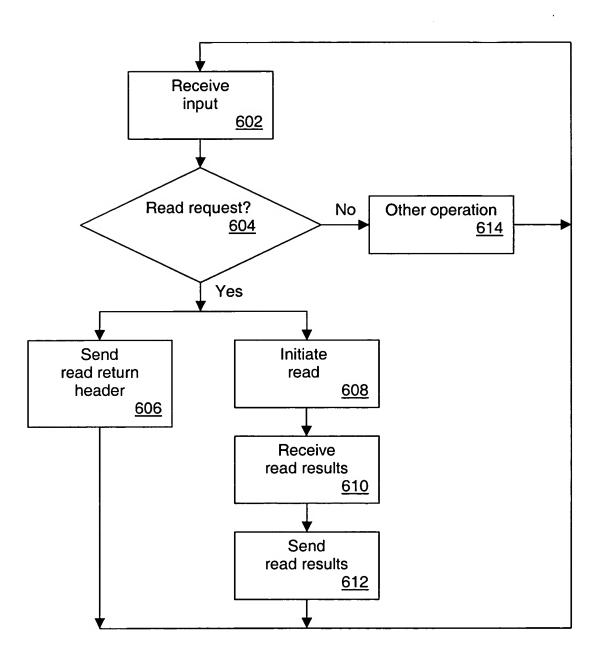
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FIG. 6A



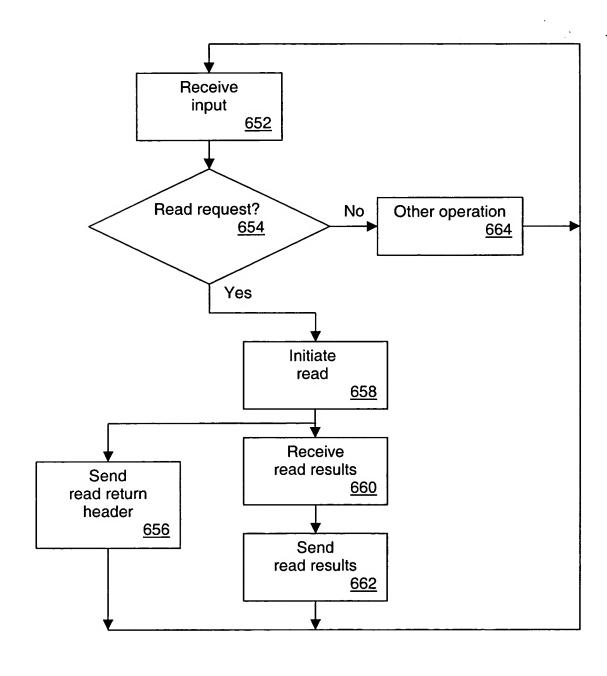


FIG. 6B

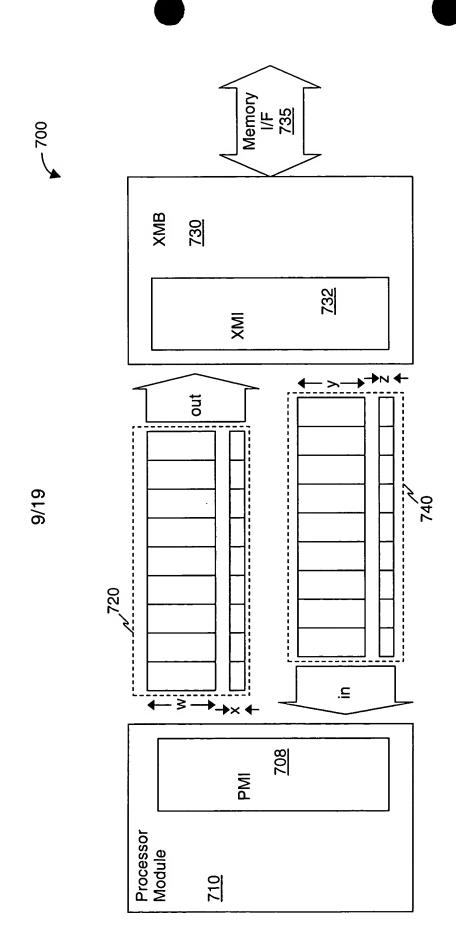


FIG. 7

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FIG. 8

FIG. 9

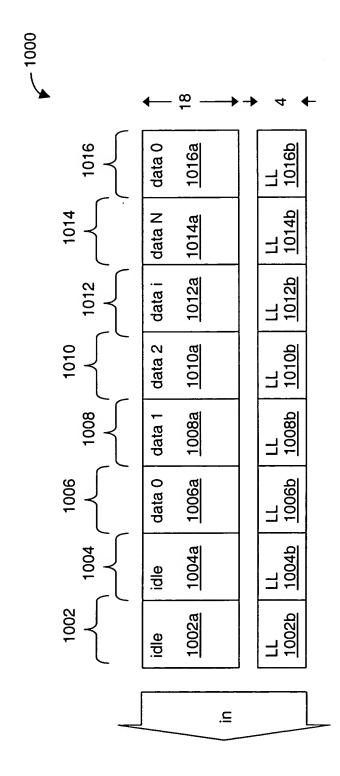


FIG. 10

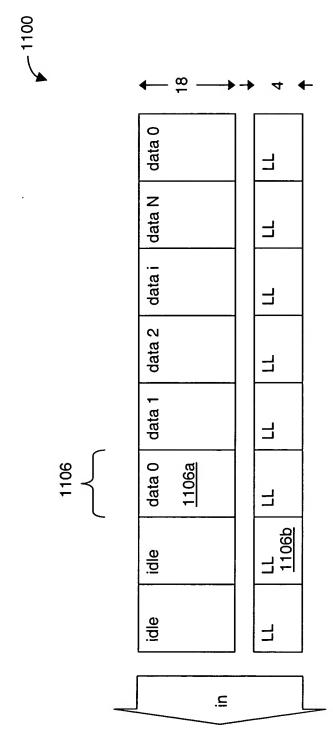


FIG. 11

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**√** 1200

FIG. 12

↓Transfer	BITS 17:0
	Lower Order Address Bits and Read/Write Command
	Higher Order Address Bits and Early Read Indicator
2	Command Destination, Offset from Address, Transaction ID, Check Bits, Mask Bits, Stream ID,
က	Size Bits, Cancel Command, Priority, etc.

FIG. 13

	• Command	d Indicator	oto Bite Mack Bite Cizo Bite ofe	den Dits, Mash Dits, Olde Dits, die.
BITS 17:0	Lower Order Address Bits and Read/Write Command	Higher Order Address Bits and Early Read Indicator	Common Destination Officet from Address Transcotion ID Chack Bite Mack Bite Stro Bite at	Collinaid Destination, Cliset Holl Address, Transaction ID, C
↓Transfer	0	1	2	ဧ

FIG. 14

BITS 17:0	Lower Order Configuration Address Bits and Read/Write Command	Higher Order Configuration Address Bits and Early Read Indicator	oto off office long Angel Transfer of State Man Dita of	COMMISSION DESIMISMON, ONSELHOM AUGIESS, Mansachol ID, ONECA DNS, Massachol Company of C
	o¬	Ή		
↓Transfer	0	-	0	ო

FIG. 15



1600

LL signals	Transfers 0:3	
0	Info and Check Bits	
1	Header, Tail, and Check Bits	
2	Extended Mode Bits	
3	Exteriued Mode Dits	

FIG. 16

LL signals	Transfers 0:3
0	Type and Check Bits
1	Type and Oneck bits
2	Info and Check Bits
3	IIIIO and Check bits

FIG. 17



LL signals	Transfers 0:3	
0	Tag, Control, and Check Bits	
1	rag, Control, and Check bits	
2	Tog. Info. and Chook Pita	
3	Tag, Info, and Check Bits	

FIG. 18

1900

FIG. 19

2000

LL signal